

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
10/771,749	02/03/2004	Hyoung-Sub Kim	8750-063	3653
20575 7590 09/22/2005			EXAMINER	
	HNSON & MCCOLLC	CAO, PHAT X		
210 SW MORRISON STREET, SUITE 400 PORTLAND, OR 97204			ART UNIT	PAPER NUMBER
			2814	

DATE MAILED: 09/22/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		\mathcal{W}
	Application No.	Applicant(s)
	10/771,749	KIM, HYOUNG-SUB
Office Action Summary	Examiner	Art Unit
	Phat X. Cao	2814
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the	he correspondence address
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING D Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication If NO period for reply is specified above, the maximum statutory period - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b). Status 1) Responsive to communication(s) filed on 22 January	ATE OF THIS COMMUNICAT 36(a). In no event, however, may a reply to will apply and will expire SIX (6) MONTHS a, cause the application to become ABAND g date of this communication, even if timely uly 2005.	Pion. Tion in the mailing date of this communication. ONED (35 U.S.C. § 133). Filed, may reduce any
closed in accordance with the practice under E		
Disposition of Claims 4) ○ Claim(s) 1-20 is/are pending in the application 4a) Of the above claim(s) 14-20 is/are withdraw 5) ○ Claim(s) is/are allowed. 6) ○ Claim(s) 1-13 is/are rejected. 7) ○ Claim(s) is/are objected to. 8) ○ Claim(s) are subject to restriction and/or are subject to restriction and/or are subjected to by the Examine 10) ○ The drawing(s) filed on is/are: a) ○ according and are subjected to by the Examine 10) ○ The drawing(s) filed on is/are: a) ○ according and are subjected to by the Examine 10) ○ The drawing(s) filed on is/are: a) ○ according to the Replacement drawing sheet(s) including the correction of the corre	wn from consideration. or election requirement. er. epted or b) objected to by the drawing(s) be held in abeyance. tion is required if the drawing(s) is	See 37 CFR 1.85(a). s objected to. See 37 CFR 1.121(d).
•	xamilier. Note the attached Of	nde Action of form 1 10-132.
Priority under 35 U.S.C. § 119 12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Burea * See the attached detailed Office action for a list	ts have been received. ts have been received in Appli ority documents have been rec u (PCT Rule 17.2(a)).	cation No eived in this National Stage
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 2/3/04.		nary (PTO-413) ail Date nal Patent Application (PTO-152)

Application/Control Number: 10/771,749

Art Unit: 2814

DETAILED ACTION

1. Applicant's election without traverse of Group I (claims 1-13) in the reply filed on 7/22/05 is acknowledged.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1-2, 4-10 and 12-13 are rejected under 35 U.S.C. 102(b) as being anticipated by Ohkawa (US. 6,091,154).

Regarding claims 1, 5, 8 and 9, Ohkawa (Fig. 19C) discloses a semiconductor device comprising: a semiconductor substrate 40 having a cell array region and a peripheral circuit region (column 23, lines 35-39); a plurality of word line patterns 48 placed on the cell array region; a gate pattern 48 placed on the peripheral circuit region; an interlayer insulating layer 63 of BPSG (column 23, lines 30-31) covering an upper surface of the semiconductor substrate 40 having the word line patterns and the gate pattern 48; a self-aligned contact spacer 66 covering a sidewall of the self-aligned contact hole; and gate spacers 60 interposed between sidewalls of the gate pattern 48 and the interlayer insulating layer 63, a width of the gate spacers 60 of 50 to 200 nm (column 23, lines 12-14) being substantially different and greater than from a width of the self-aligned contact spacer 66 of 30 to 100 nm (column 23, lines 1-3).

Application/Control Number: 10/771,749

Art Unit: 2814

Regarding claims 2 and 10, Ohkawa (Fig. 19C) further discloses the word line spacers 60 interposed between sidewalls of the word line patterns 48 placed opposite to the self-aligned contact hole 64 and the interlayer insulating layer 63, the word line spacers 60 being formed of the same material layer as the gate spacers 60, the word line spacers 60 having the same width as that of the gate spacers 60.

Regarding claims 4 and 12, Ohkawa (Fig. 19C) further discloses a contact etch stop layer 62 interposed between the word line spacers 60 and the interlayer insulating layer 63, and between the gate spacers 60 and the interlayer insulating layer 63.

Regarding claim 6, Ohkawa (Fig. 19C) also discloses that the self-aligned contact hole 64 includes a lower contact hole formed at a region between the word line patterns 48, and an upper contact hole placed on the lower contact hole and formed to penetrate the interlayer insulating layer 63.

Regarding claims 7 and 13, Ohkawa (Fig. 20C) also discloses that the selfaligned contact hole 51 having the diameter of the lower contact hole is less than that of the upper contact hole in a direction across the word line patterns.

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 3 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ohkawa (US. 6,091,154) in view of Chang et al (US. 5,817,562).



Application/Control Number: 10/771,749

Art Unit: 2814

Ohkawa does not disclose a spacer etch stop layer as claimed.

However, Chang (Fig. 5) teaches the forming of a spacer etch stop layer 24 between the word line spacer 26/28 and one of the two word line patterns 16, between the gate spacer 26/28 and the gate pattern 16, and between the self-aligned contact spacer 30 and the two word line patterns 16. Accordingly, it would have been obvious to modify the device of Ohkawa by forming a spacer etch stop layer with the structures as set forth above because such forming a structure of spacer etch stop layer would stop the etching through the substrate during the forming of the sidewall spacers 26/28, as taught by Chang (see Fig. 4 and column 6, lines 17-25).

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phat X. Cao whose telephone number is 571-272-1703. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on 571-272-1705. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

2.

Application/Control Number: 10/771,749

Art Unit: 2814

Page 5

PC

September 16, 2005

PHAT X. CAO